

SOLVING HARD PROBLEMS QUICKLY USING SAT SOLVERS

Martin Hořeňovský

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ABOUT THIS TALK

This talk is about solving real world problems using SAT solvers.

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SAT solvers will be used as a black box and we will not cover any of the theory behind them.

We will start by going over the boolean satisfaction (SAT) problem.

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Then we will learn how to drive a SAT solver from C++.

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Then we will learn how to drive a SAT solver from C++.

Then we will apply our newly gained knowledge to two practical examples, Sudoku and Master Key Systems.

INTRODUCTION TO SAT

The boolean satisfaction problem (SAT) is about checking whether a logical formula is *satisfiable*.

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A formula is *satisfiable* if we can assign values to its variables so that the whole formula is true.

```
if (A || B || (!A && !C)) {  
    create_new_widget();  
} else {  
    reuse_old_widget();  
}
```

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```

if (A || B || (!A && !C))

$A \vee B \vee (\neg A \wedge \neg C)$

LOGICAL OPERATORS

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Negation, also known as NOT [$!A$]

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Negation, also known as NOT [$\neg A$]

$$\neg \alpha$$

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Disjunction, also known as OR [$A \mid \mid B$]

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LOGICAL OPERATORS

Negation, also known as NOT [!A]

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Disjunction, also known as OR [A || B]

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Conjunction, also known as AND [A && B]

LOGICAL OPERATORS

Negation, also known as NOT [!A]

$$\neg \alpha$$

Disjunction, also known as OR [A || B]

$$\alpha \vee \beta$$

Conjunction, also known as AND [A && B]

$$\alpha \wedge \beta$$

IMPLICATION

IMPLICATION

$$\alpha \implies \beta$$

IMPLICATION

$$\alpha \implies \beta$$

α	β	$\alpha \implies \beta$
1	1	1
1	0	0
0	1	1
0	0	1

EQUIVALENCE

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$$\alpha \iff \beta$$

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α	β	$\alpha \iff \beta$
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There is a small problem; SAT solvers do not accept arbitrary logical formulae.

They only accept logical formulae in the *Conjunctive Normal Form* (CNF).

Conjunctive Normal Form (CNF) means that the formula is a *conjunction of disjunctive* clauses.

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In other words, the formula is an AND of many ORs.

$$A \vee B \vee (\neg A \wedge \neg C)$$

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How do we convert this to CNF?

CONVERSIONS

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Every formula can be converted into an *equivalent* CNF formula.

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Every formula can be converted into an *equivalent* CNF formula.

It helps if you know De Morgan's laws, distributive laws and some simple identities.

Original clause

Equivalent clause

$$\neg\neg\alpha$$

$$\alpha$$

$$\neg(\alpha \wedge \beta)$$

$$\neg\alpha \vee \neg\beta$$

$$\neg(\alpha \vee \beta)$$

$$\neg\alpha \wedge \neg\beta$$

$$(\alpha \wedge \beta) \vee \gamma$$

$$(\alpha \vee \gamma) \wedge (\beta \vee \gamma)$$

$$(\alpha \vee \beta) \wedge \gamma$$

$$(\alpha \wedge \gamma) \vee (\beta \wedge \gamma)$$

$$\alpha \implies \beta$$

$$\neg\alpha \vee \beta$$

$$\alpha \iff \beta$$

$$(\alpha \implies \beta) \wedge (\alpha \impliedby \beta)$$

$$A \vee B \vee (\neg A \wedge \neg C)$$

$$A \vee B \vee (\neg A \wedge \neg C)$$

$$\gamma \vee (\alpha \wedge \beta)$$

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$$(A \vee B \vee \neg A) \wedge (A \vee B \vee \neg C)$$

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$$\gamma \vee (\alpha \wedge \beta)$$

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$$(A \vee B \vee \neg A) \wedge (A \vee B \vee \neg C)$$

$$(A \vee B \vee \neg C)$$

That's all we need to know about (CNF-)SAT.

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At least for now.

HOW TO DRIVE SAT SOLVER FROM C++

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<https://github.com/master-keying/minisat>

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<https://github.com/master-keying/minisat>

It is also in vcpkg as "minisat-master-keying".

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- `Var` - The representation of a logic *variable*

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- `Solver` - The solver itself
 - `Vec` - A relocating implementation of `std::vector`
- and 2 vocabulary types
- `Var` - The representation of a logic *variable*
 - `Lit` - The concrete *literal* of a variable in a clause

$$(A \vee B \vee \neg A) \wedge (A \vee B \vee \neg C)$$

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3 variables, A , B , and C

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3 variables, A , B , and C

4 literals, A , B , $\neg A$, and $\neg C$.

Let's solve the formula

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```
#include <minisat/core/Solver.h>
#include <iostream>

int main() {
    using Minisat::mkLit; using Minisat::lbool;

    Minisat::Solver solver;

    auto A = solver.newVar();
    auto B = solver.newVar();
    auto C = solver.newVar();

    solver.addClause( mkLit(A),  mkLit(B),  ~mkLit(A) );
    solver.addClause( mkLit(A),  mkLit(B),  ~mkLit(C) );
```

... and then retrieve the results

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```
auto sat = solver.solve();
if (sat) {
    std::cout << "SAT\n"
                << "Model found:\n"
                << "A := " << (solver.modelValue(A) == l_True) <<
                << "B := " << (solver.modelValue(B) == l_True) <<
                << "C := " << (solver.modelValue(C) == l_True) <<
} else {
    std::cout << "UNSAT\n";
    return 1;
}
}
```

So what solution did Minisat find?

So what solution did Minisat find?

```
$ ./example-1  
SAT  
Model found:  
A := 0  
B := 0  
C := 0
```

Now we know enough to make a Sudoku solver.

HOW TO CONVERT SUDOKU TO SAT

Sudoku is a puzzle where you put numbers 1-9 onto a 9x9 grid, split into 9 3x3 boxes

Some of the numbers are prefilled and we have to fill in the rest, following some simple rules:

5	3			7				
6			1	9	5			
	9	8					6	
8				6				3
4			8		3			1
7				2				6
	6					2	8	
			4	1	9			5
				8			7	9

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			4	1	9			5
				8			7	9

1. Each row contains all of the numbers 1-9
2. Each column contains all of the numbers 1-9
3. Each 3x3 box contains all of the numbers 1-9

When translating these rules into SAT, we have to start by defining the variables.

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In SAT, variable can have 2 values, "true", or "false".

The solution is to have a variable per each position
and each possible value.

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Let's denote these variables as $x_{r,c}^v$

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Let's denote these variables as $x_{r,c}^v$

If the variable $x_{r,c}^v$ is set to true, the r -th row and c -th
column contains number v .

1. EACH ROW CONTAINS ALL OF THE NUMBERS 1-9

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$$\forall (r, v) \in (\text{rows} \times \text{values}) : x_{r,1}^v \vee x_{r,2}^v \vee \dots \vee x_{r,9}^v$$

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$$\forall (r, v) \in (\text{rows} \times \text{values}) : \bigvee_{i=1}^9 x_{r,i}^v$$

2. EACH COL CONTAINS ALL OF THE NUMBERS 1-9

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$$\forall (c, v) \in (\text{columns} \times \text{values}) : x_{1,c}^v \vee x_{2,c}^v \vee \dots \vee$$

$$\forall (c, v) \in (\text{columns} \times \text{values}) : \bigvee_{i=1}^9 x_{i,c}^v$$

3. EACH BOX CONTAINS ALL OF THE NUMBERS 1-9

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$$\forall (b, v) \in (\text{boxes} \times \text{values}) : x_{br_1, bc_1}^v \vee x_{br_1, bc_2}^v \vee \dots$$

$$\forall (b, v) \in (\text{boxes} \times \text{values}) : \bigvee_{(r,c) \in b} x_{r,c}^v$$

We expressed the Sudoku rules as a set of clauses.

We expressed the Sudoku rules as a set of clauses.
But an important set of clauses is missing.

1 4 7	2 5 8	3 6 9						
			1 4 7	2 5 8	3 6 9			
						1 4 7	2 5 8	3 6 9
	1 4 7	2 5 8	3 6 9					
				1 4 7	2 5 8	3 6 9		
							1 4 7	2 5 8
		1 4 7	2 5 8	3 6 9				
					1 4 7	2 5 8	3 6 9	
								1 4 7
								2 5 8
								3 6 9

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This assumption was lost when we split each position into multiple different variables.

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We need to add it back.

4. EACH POSITION CONTAINS EXACTLY ONE NUMBER

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$\forall (r, c) \in (\text{rows} \times \text{columns}) : \text{exactly-one}(x_{r,c}^1, \dots)$

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$\forall (r, c) \in (\text{rows} \times \text{columns}) : \text{exactly-one}(x_{r,c}^1, \dots)$

The `exactly-one` helper adds a set of clauses that allows only one of the literals to be true.

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The `exactly-one` helper adds a set of clauses that allows only one of the literals to be true.

Let's take a look at how it works.

We cannot directly limit the number of true literals.

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But we can place lower and upper limits on them.

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But we can place lower and upper limits on them.

In other words, *exactly one* literal is true when *at least one* is true **and** *at most one* is true.

Making *at least one* literal true is simple:

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$$\bigvee_{lit \in \text{literals}} lit$$

Forcing *at most one* literal to be true is based on a simple observation

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At most one literal is true when *there is no pair of literals where both literals are true at the same time.*

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At most one literal is true when *there is no pair of literals where both literals are true at the same time.*

$\forall l_1 \in \text{literals},$

$l_2 \in \text{literals},$

$l_1 \neq l_2 : \neg (l_1 \wedge l_2)$

Let's write us a C++ Sudoku solver.

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All the code in this section can be found at
<https://github.com/horenmar/sudoku-example>

First we need to figure out addressing variables.

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SAT solvers see variables as integers in range $0..N$.

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Luckily, we can easily map $x_{r,c}^v$ into an integer as

$$r * 9 * 9 + c * 9 + v$$

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SAT solvers see variables as integers in range 0..N.
Luckily, we can easily map $x_{r,c}^v$ into an integer as

$$r * 9 * 9 + c * 9 + v$$

```
Minisat::Var toVar(int row, int column, int value) {  
    return row * columns * values + column * values + value;  
}
```

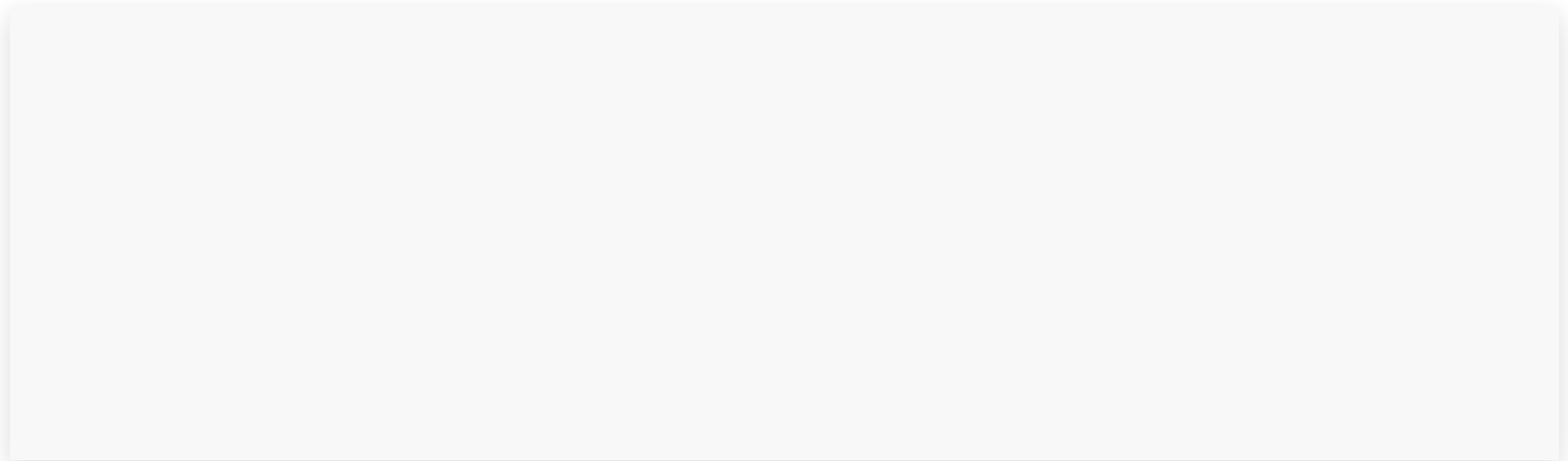
Before we can start adding clauses, we need to
allocate all variables

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```
void Solver::init_variables() {
    for (int r = 0; r < rows; ++r) {
        for (int c = 0; c < columns; ++c) {
            for (int v = 0; v < values; ++v) {
                static_cast<void>(solver.newVar());
            }
        }
    }
}
```

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for (int row = 0; row < rows; ++row) {  
    for (int value = 0; value < values; ++value) {  
        Minisat::vec<Minisat::Lit> literals;  
  
    }  
}
```

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for (int row = 0; row < rows; ++row) {
    for (int value = 0; value < values; ++value) {
        Minisat::vec<Minisat::Lit> literals;
        for (int col = 0; col < columns; ++col) {
            literals.push(Minisat::mkLit(toVar(row, col, value
        }
    }
}
```

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for (int row = 0; row < rows; ++row) {
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        for (int col = 0; col < columns; ++col) {
            literals.push(Minisat::mkLit(toVar(row, col, value
        }
        solver.addClause(literals);
    }
}
```

2. EACH COL CONTAINS ALL OF THE NUMBERS 1-9

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```
for (int col = 0; col < columns; ++col) {
    for (int value = 0; value < values; ++value) {
        Minisat::vec<Minisat::Lit> literals;
        for (int row = 0; row < rows; ++row) {
            literals.push(Minisat::mkLit(toVar(row, col, value
        }
        solver.addClause(literals);
    }
}
```


3. EACH BOX CONTAINS ALL OF THE NUMBERS 1-9

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```
for (int value = 0; value < values; ++value) {
  for (int r : {0, 3, 6}) {
    for (int c : {0, 3, 6}) {
      Minisat::vec<Minisat::Lit> literals;
      for (int rr : {0, 1, 2}) {
        for (int cc : {0, 1, 2}) {
          literals.push(Minisat::mkLit(
            toVar(r + rr, c + cc, value)
          ));
        }
      }
      solver.addClause(literals);
    }
  }
}
```

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for (int row = 0; row < rows; ++row) {
    for (int col = 0; col < columns; ++col) {
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        for (int value = 0; value < values; ++value) {
            literals.push(Minisat::mkLit(toVar(row, col, value)
        }
        exactly_one(literals);
    }
}
```

```
void Solver::exactly_one(Minimat::vec<Minimat::Lit> const& lits)
// At least one
solver.addClause(lits);

// At most one
for (size_t i = 0; i < lits.size(); ++i) {
    for (size_t j = i + 1; j < lits.size(); ++j) {
        solver.addClause(~lits[i], ~lits[j]);
    }
}
}
```

We have a model of Sudoku as a SAT instance.

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Now we need to insert an actual instance of the puzzle,
and then extract the solution.

Inserting an instance is easy enough, each prefilled square gets an unary clause:

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```
bool Solver::apply_board(board const& b) {
    for (int row = 0; row < rows; ++row) {
        for (int col = 0; col < columns; ++col) {
            auto value = b[row][col];
            if (value != 0) {
                solver.addClause(
                    Minisat::mkLit(toVar(row, col, value - 1))
                );
            }
        }
    }
    return ret;
}
```

Extracting a solution is similarly simple, we just need to check which variable for a given square is "true".

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```
board Solver::get_solution() const {
    board b(rows, std::vector<int>(columns));
    for (int row = 0; row < rows; ++row) {
        for (int col = 0; col < columns; ++col) {
            for (int val = 0; val < values; ++val) {
                if (solver.modelValue(toVar(row, col, val)).isTrue)
                    b[row][col] = val + 1;
                break;
            }
        }
    }
    return b;
}
```

Let's take a look at how our solver performs.

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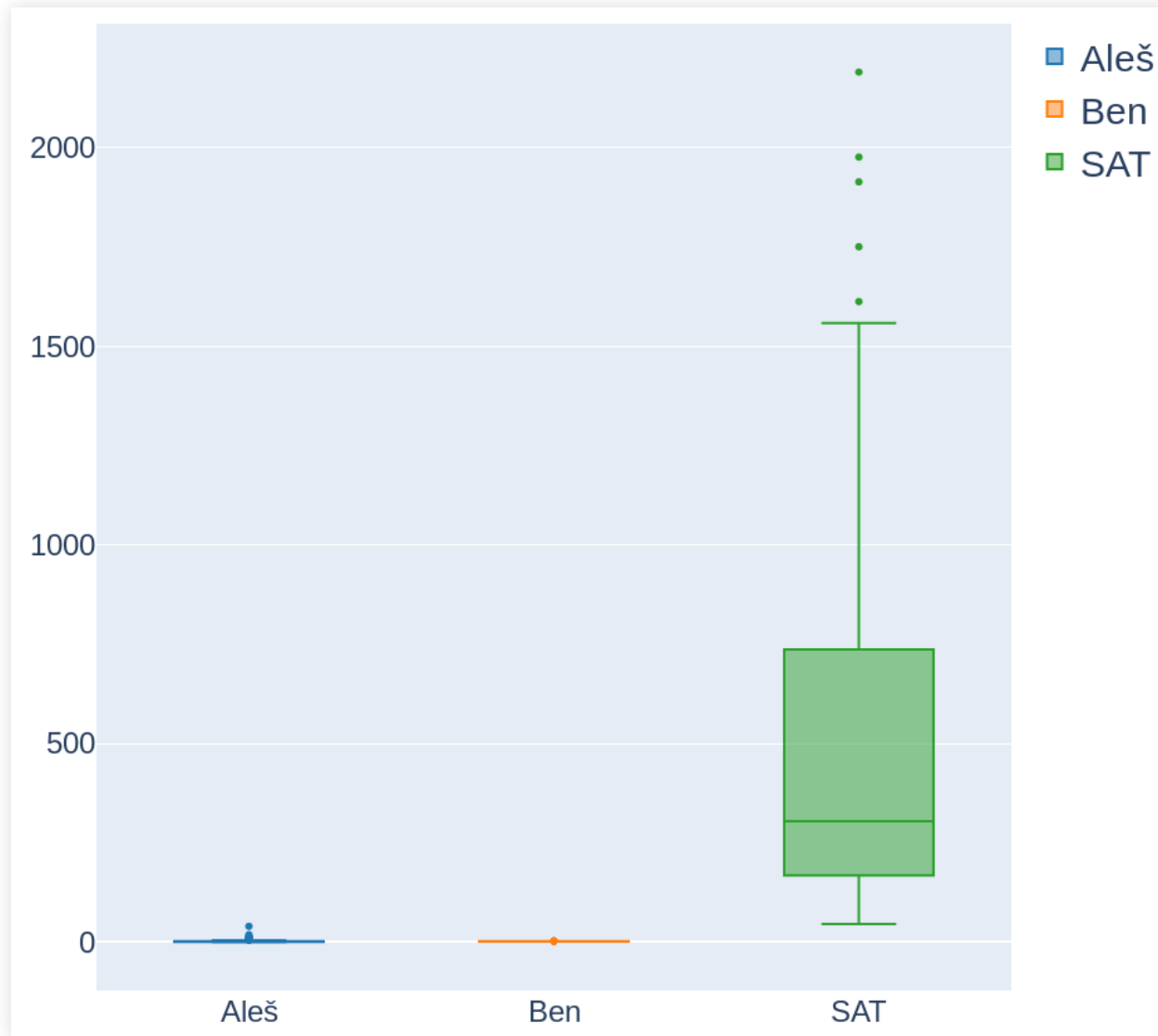
All benchmarks were run on the same machine, and the binaries were compiled with g++ under WSL.

Let's take a look at how our solver performs.

All benchmarks were run on the same machine, and the binaries were compiled with g++ under WSL.

The inputs were 95 "hard" instances of Sudoku.

Runtimes of different solvers [ms]



Counterintuitively, giving a SAT solver less clauses and/or variables can slow it down.

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Let's see what happens when we encode the sudoku rules differently, and give the solver more information.

1. Each row contains each of the numbers 1-9

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3. Each 3x3 box contains each of the numbers 1-9

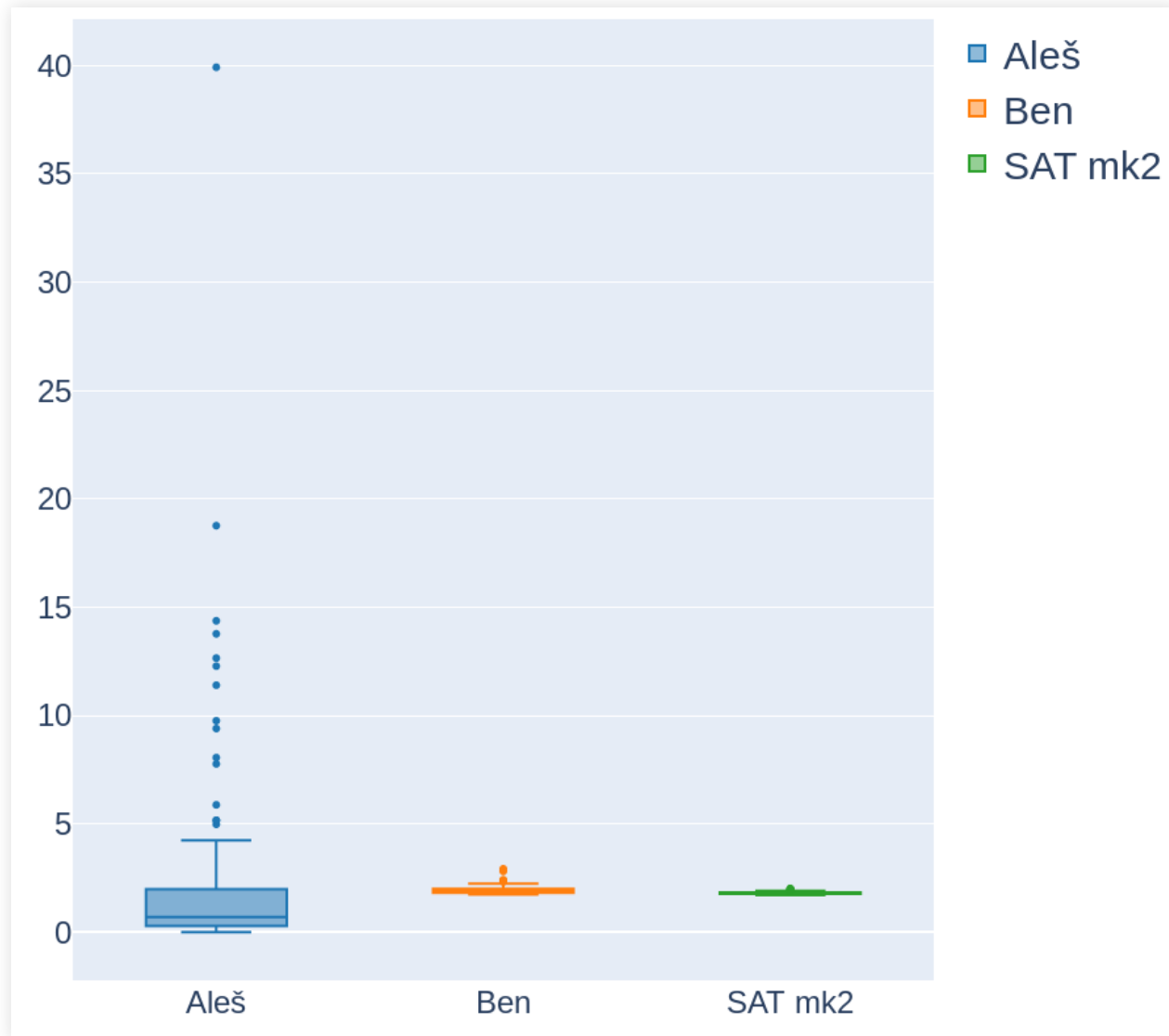
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for (int row = 0; row < rows; ++row) {
    for (int value = 0; value < values; ++value) {
        Minisat::vec<Minisat::Lit> literals;
        for (int col = 0; col < columns; ++col) {
            literals.push(Minisat::mkLit(toVar(row, col, value
        }
        solver.addClause(literals);
    }
}
```



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for (int row = 0; row < rows; ++row) {
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    for (int col = 0; col < columns; ++col) {
      literals.push(Minisat::mkLit(toVar(row, col, value
    }
    solver.addClause(literals);
    exactly_one(literals);
  }
}
```

Runtimes of different solvers [ms]



RECAP

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Be careful to encode *all* of your assumptions.

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More clauses does not mean worse performance.

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More clauses does not mean worse performance.

But it does not mean better performance either.

RECAP

Be careful to encode *all* of your assumptions.

More clauses does not mean worse performance.

But it does not mean better performance either.

Experiment with different encodings.

HOW TO SOLVE A MASTER KEY SYSTEM WITH A SAT SOLVER

Master Key System (MKS) is a set of keys and locks where a key can open more than one lock.

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The relations between keys and locks can be arbitrarily complex, and are described in a lockchart.

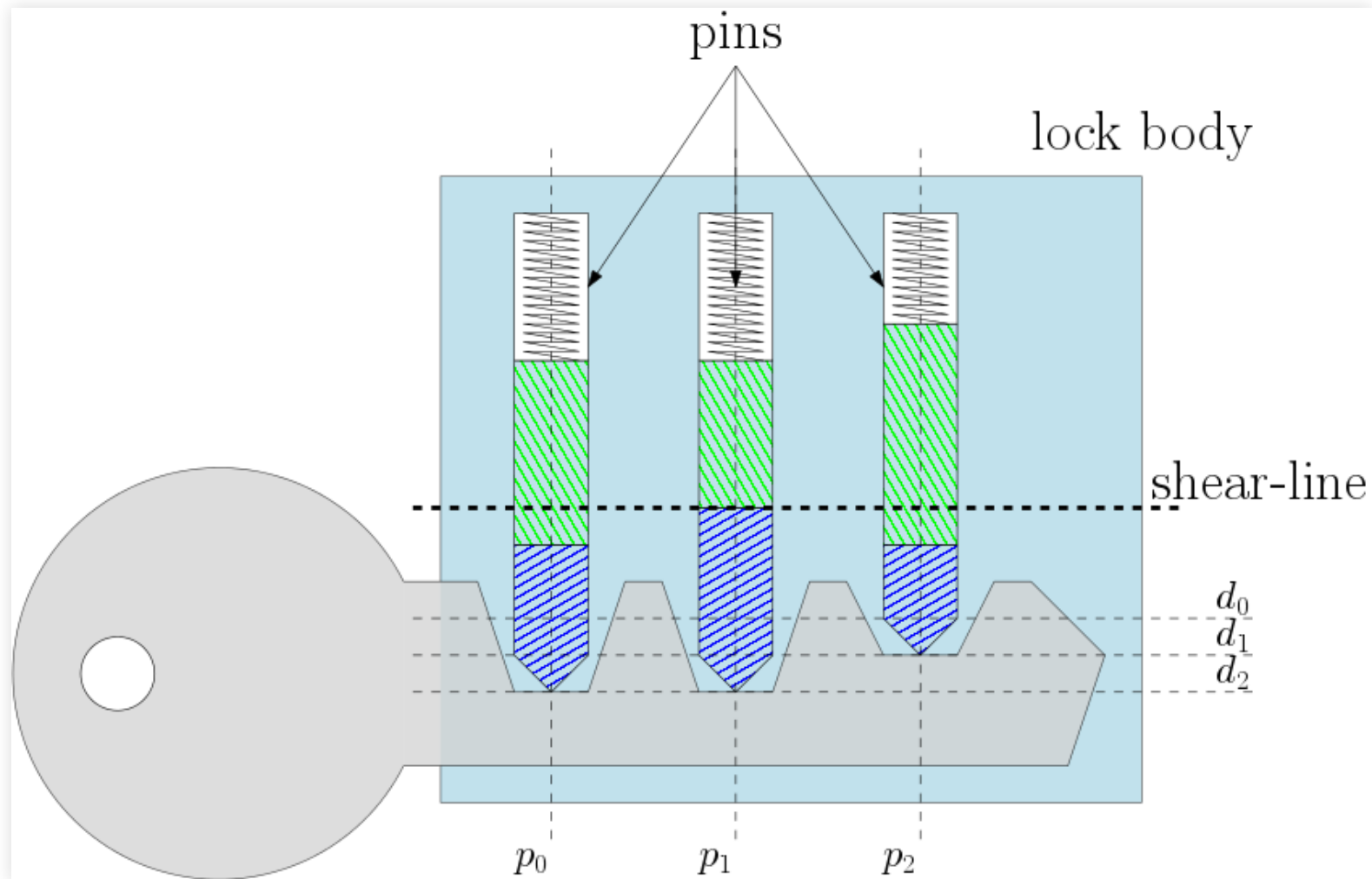
The common depiction of a lockchart is a simple table:

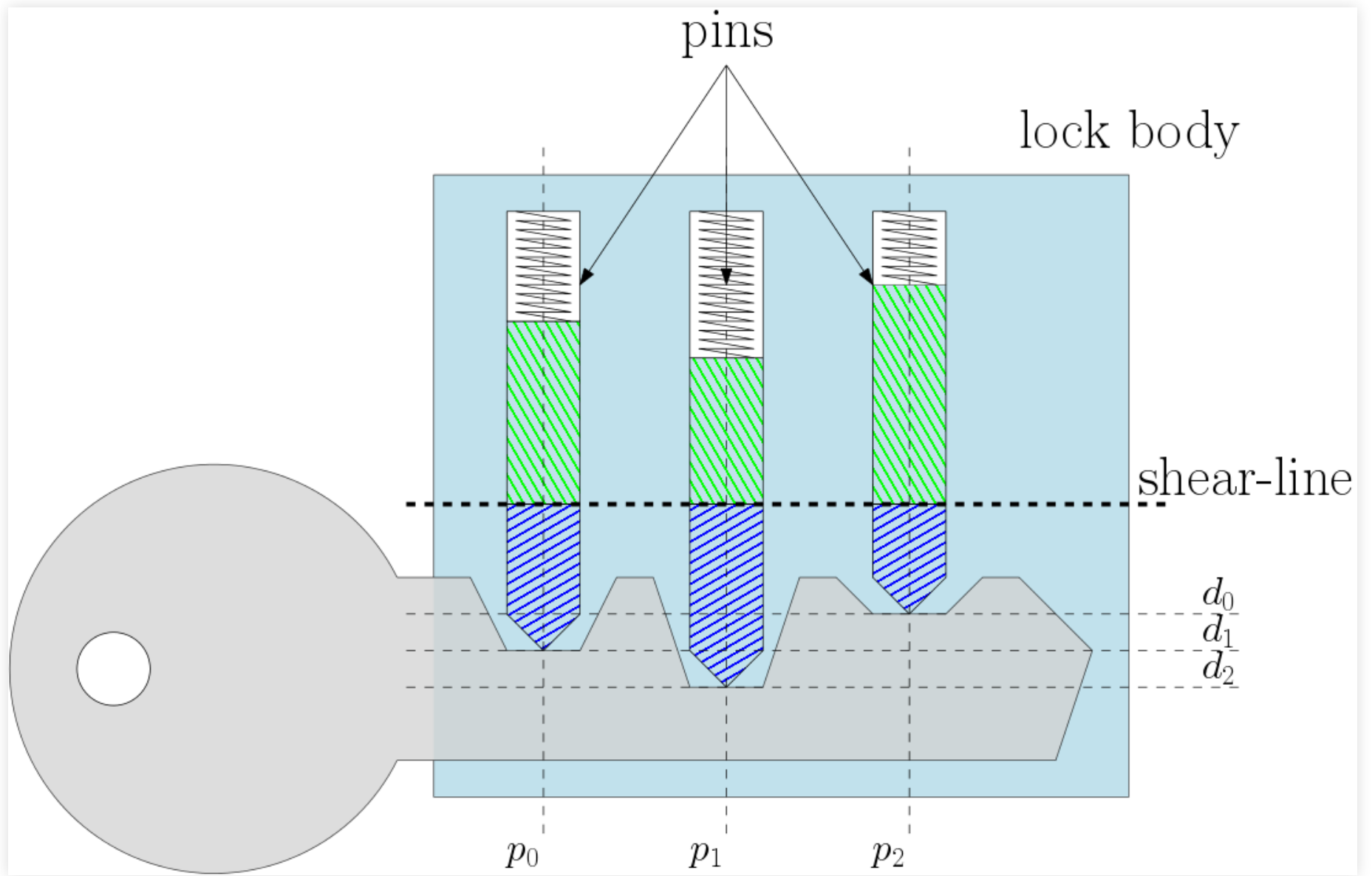
	G	M_1	M_2	K_1	K_2	K_3	K_4	K_5	K_6	K_7	K_8
L_1	■	■	□	■	□	□	□	□	□	□	□
L_2	■	■	□	□	■	□	□	□	□	□	□
L_3	■	■	□	□	□	■	□	□	□	□	□
L_4	■	■	□	□	□	□	■	□	□	□	□
L_5	■	□	■	□	□	□	□	■	□	□	□
L_6	■	□	■	□	□	□	□	□	■	□	□
L_7	■	□	■	□	□	□	□	□	□	■	□
L_8	■	□	■	□	□	□	□	□	□	□	■
GL	■	■	■	■	■	■	■	■	■	■	■

Each MKS has a geometry associated with it.

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The geometry describes the positions, their depths,
and the constraints the keys must satisfy.

Let's take a look at the geometry and inner working of a *pin tumbler* lock.





THE RULES

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5. A key's cutting must satisfy all constraints

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$lock_{p,d}^l$, which is true when lock l is cut at depth d in position p

1. A KEY HAS EXACTLY ONE CUTTING DEPTH AT A POSITION

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$\forall (k, p) \in (\text{keys} \times \text{positions}) :$
 $\text{exactly-one}(key_{p,0}^k, key_{p,1}^k, \dots, key_{p,d}^k)$

**2. A LOCK MUST HAVE AT LEAST ONE CUTTING DEPTH
SELECTED FOR EACH POSITION**

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$$\forall (l, p) \in (\text{locks} \times \text{positions}) : \bigvee_{d \in \text{depths}(p)} \text{lock}_{p,d}^l$$

**3. A KEY MUST OPEN ALL LOCKS THAT THE LOCK-
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$\forall k \in \text{keys},$

$\forall l \in \text{opened-by}(k) :$

$$\bigwedge_{\substack{p \in \text{positions} \\ d \in \text{depths}(p)}} \left(\text{key}_{p,d}^k \implies \text{lock} \right)$$

4. A KEY MUST BE BLOCKED IN ALL LOCKS THAT THE LOCK-CHART SPECIFIES IT SHOULD NOT OPEN

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A key opens a lock when the lock has the same cutting depths as the key.

A key is blocked in a lock when the lock is missing at least one of key's cutting depths.

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$\forall k \in \text{keys},$

$\forall l \in \text{blocked-in}(k) : \bigvee_{\substack{p \in \text{positions} \\ d \in \text{depths}(p)}} \left(\text{key}_{p,d}^k \wedge \neg \text{lock}_p^l \right)$

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This creates an exponential number of long clauses.

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We can do better.

At the start of this talk, we talked about converting formulae into equivalent formulae in CNF.

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It is also possible to create *equisatisfiable* formulae.

$\alpha \vee \beta$ and $(\alpha \vee \neg\gamma) \wedge (\gamma \vee \beta)$ are not equivalent,
but they are equisatisfiable.

We will use trick called *Tseytin transformation*.

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The idea is to introduce a new variable to represent each inner conjunction, and then disjunct those.

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Let's call this variable $block_{p,d}^{k,l}$ and define it as

$$block_{p,d}^{k,l} \iff (key_{p,d}^k \wedge \neg lock_{p,d}^l).$$

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$$(\alpha \implies \beta) \wedge (\alpha \impliedby \beta)$$

$$\mathit{block}_{p,d}^{k,l} \iff \left(\mathit{key}_{p,d}^k \wedge \neg \mathit{lock}_{p,d}^l \right)$$

$$\alpha \iff \beta$$

$$(\alpha \implies \beta) \wedge (\alpha \longleftarrow \beta)$$

$$(\neg \alpha \vee \beta) \wedge (\alpha \vee \neg \beta)$$

$$\mathit{block}_{p,d}^{k,l} \iff \left(\mathit{key}_{p,d}^k \wedge \neg \mathit{lock}_{p,d}^l \right)$$

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$$\left(\neg \mathit{block}_{p,d}^{k,l} \vee \left(\mathit{key}_{p,d}^k \wedge \neg \mathit{lock}_{p,d}^l \right) \right)$$

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We now have a model of a MKS without constraints.

We will explore 2 different constraints:

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- *Jump* constraint

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- *Key cutting hierarchy* constraint

JUMP CONSTRAINT

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To manufacture a key, the cutting depths in two adjacent positions cannot differ by more than j .

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$\forall k \in \text{keys},$

$\forall p \in \text{positions}$

$\forall d \in \text{depths}(p) :$

$$\neg \left(\text{key}_{p,d}^k \wedge \text{key}_{p+1,d+j}^k \right)$$
$$\neg \left(\text{key}_{p,d}^k \wedge \text{key}_{p+1,d-j}^k \right)$$

KEY CUTTING HIERARCHY CONSTRAINT

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For all keys k_1, k_2 , where
 $\text{opened-by}(k_2) \subset \text{opened-by}(k_1)$, for all positions
 p and depths d_1, d_2 at position p , where $d_1 < d_2$:

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For all keys k_1, k_2 , where
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 p and depths d_1, d_2 at position p , where $d_1 < d_2$:

$$\text{key}_{p,d_1}^{k_1} \implies \neg \text{key}_{p,d_2}^{k_2}$$

C++ IMPLEMENTATION

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<https://github.com/horenmar/mks-example>

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We will not go over it in this talk.

BENCHMARKS

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There are none, sorry.

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I do have an anecdote though.

~5 years ago, my university was approached by our local key manufacturer, FAB.

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A team spent ~3 years working on a specialized solver.

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A team spent ~3 years working on a specialized solver.

It could solve ~80% of test inputs.

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In 3 months, his solver could solve ~90% of the tests.

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Its later evolution is currently in production.

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SAT-based solvers can perform surprisingly well.

But specialized solvers will end up faster.

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when converting a problem to SAT.

Adding clauses can speed things up, and so can
shortening your clauses.

But there are no guarantees.

Experiment with different encodings of the problem.

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Experiment with different SAT solvers!

THE END.

QUESTIONS?

- <https://github.com/horenmar/sudoku-example>
- <https://github.com/horenmar/mks-example>
- <https://github.com/master-keying/minisat>
- <https://codingnest.com/modern-sat-solvers-fast-neat-and-underused-part-3-of-n/>